

FIG. 1 is a block diagram of a system 100.

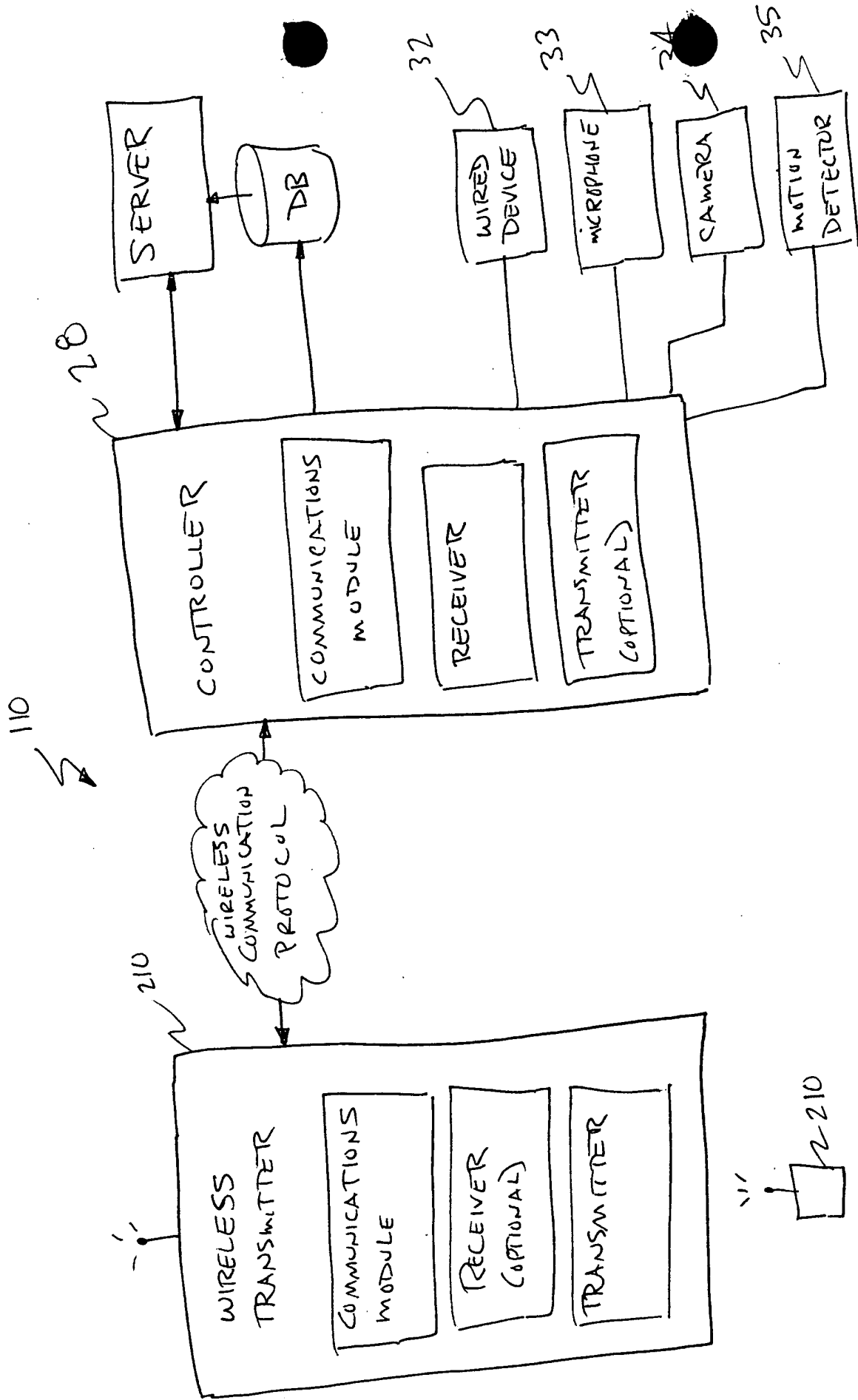


FIG. 1

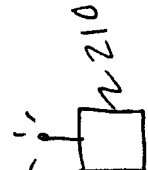
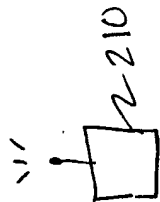


FIG. 2 is a block diagram of a system 28, including a client interface 150, a main processor 112, a micro controller 120, and a micro controller 126.

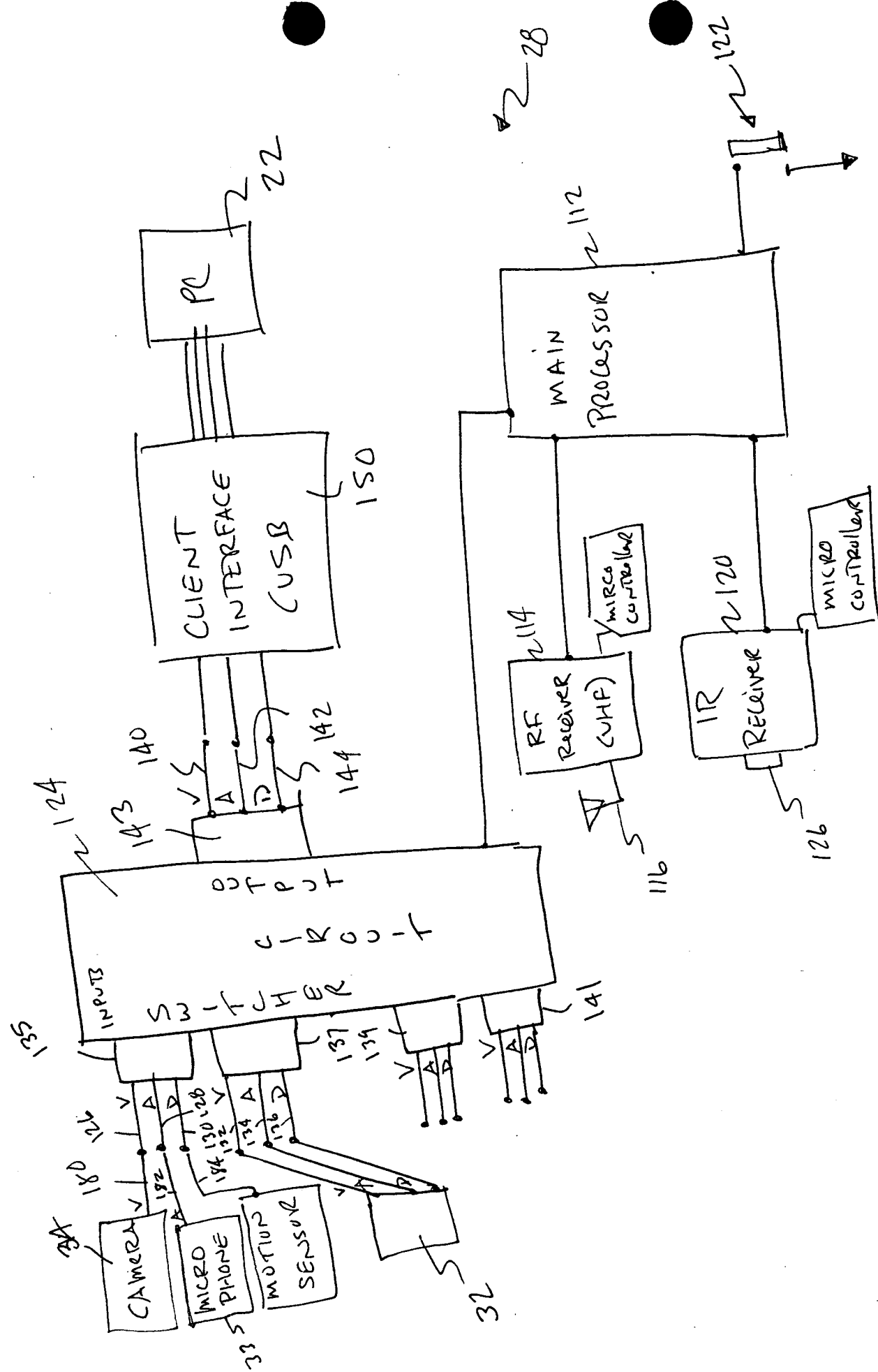


FIG. 2

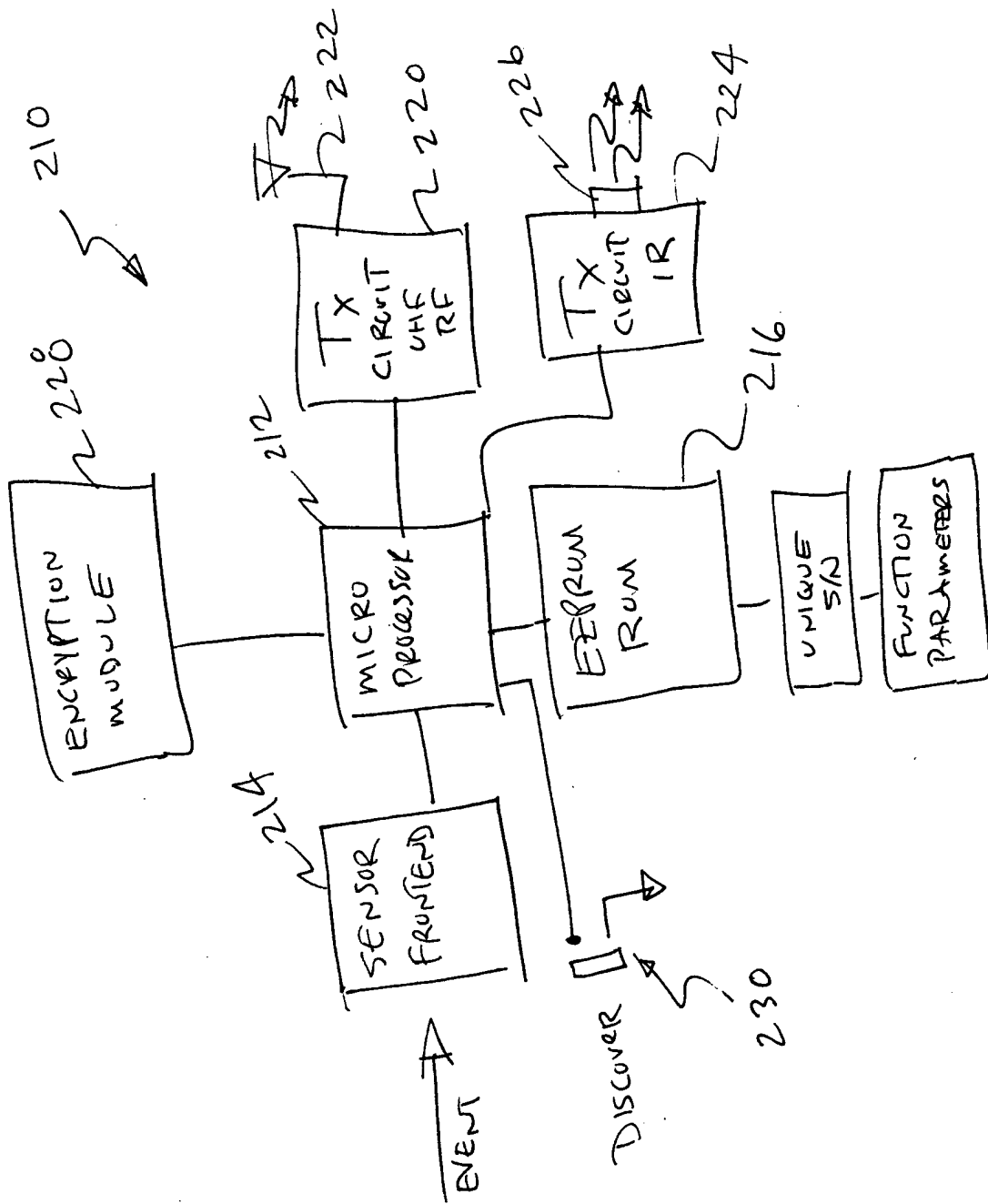


FIG 3

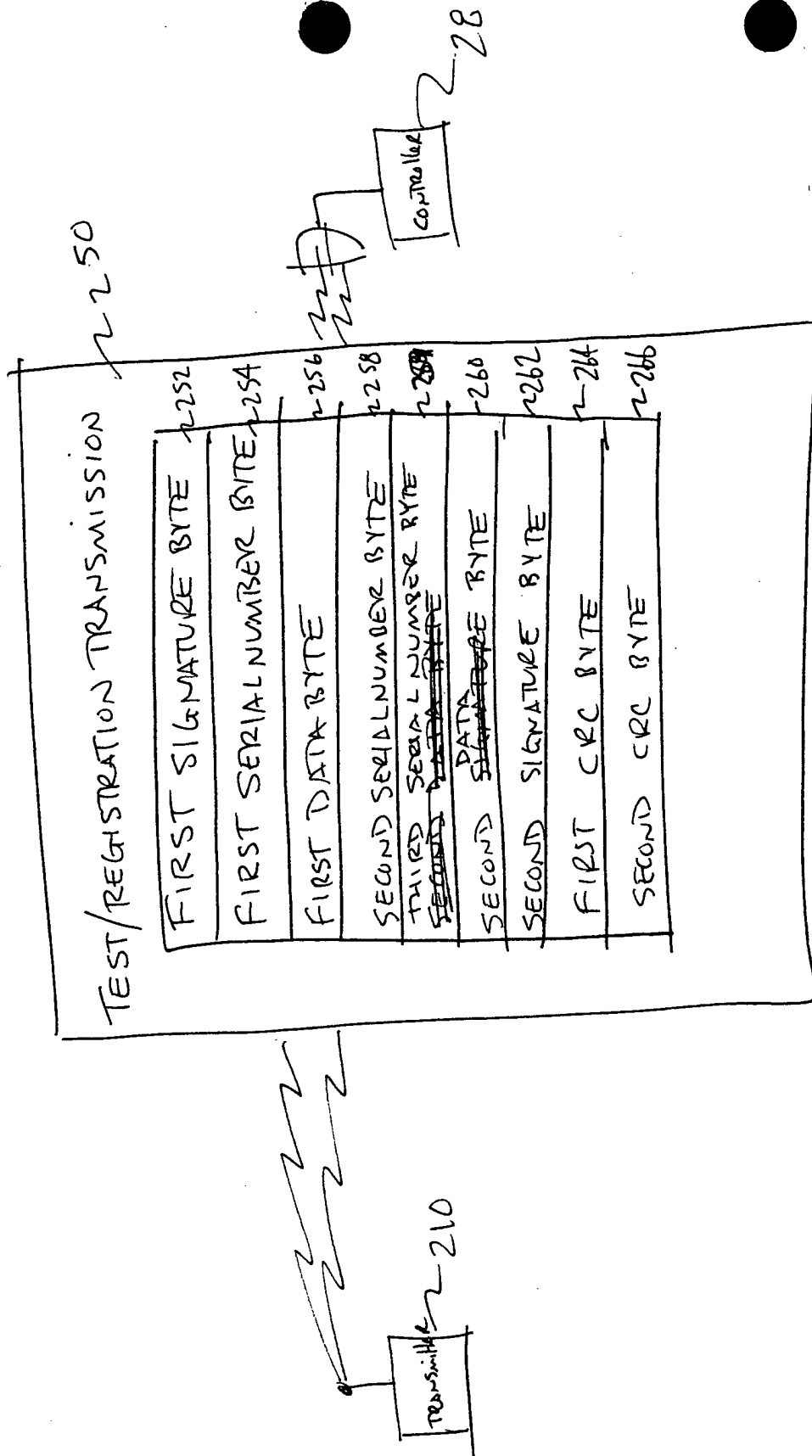


FIG. 4

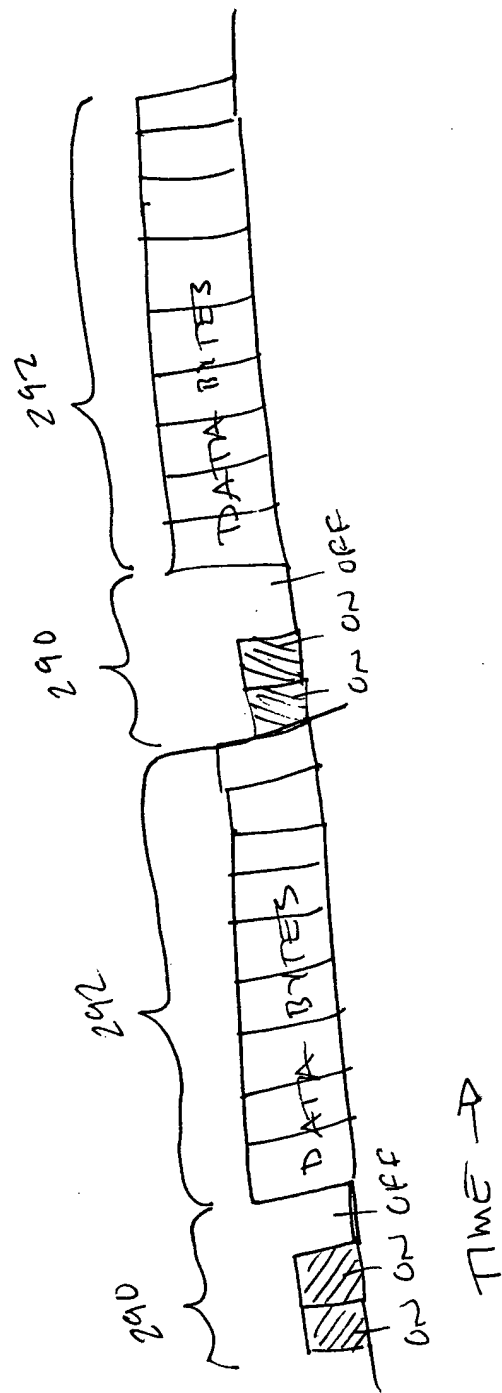


FIG. 5

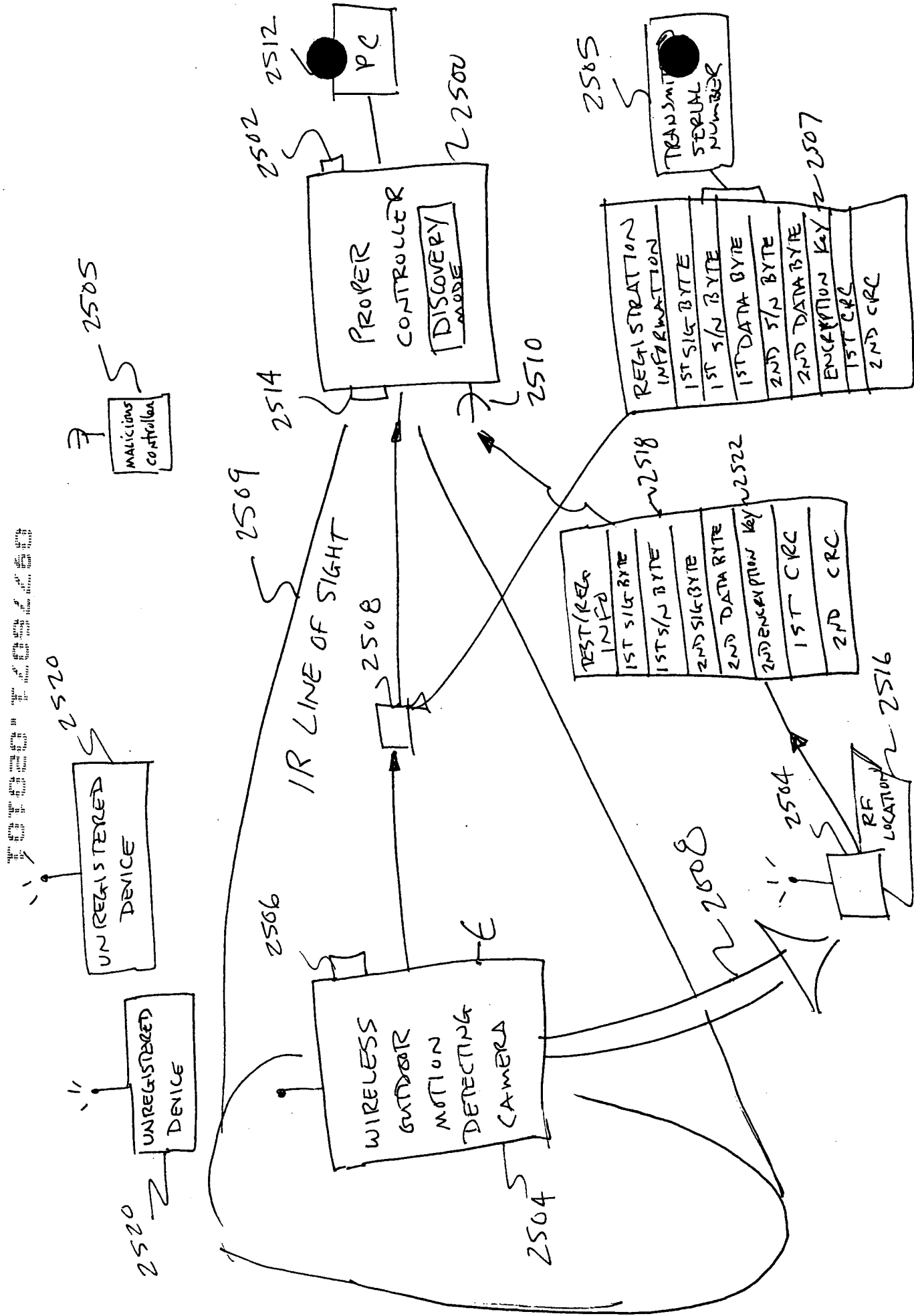


Fig 6

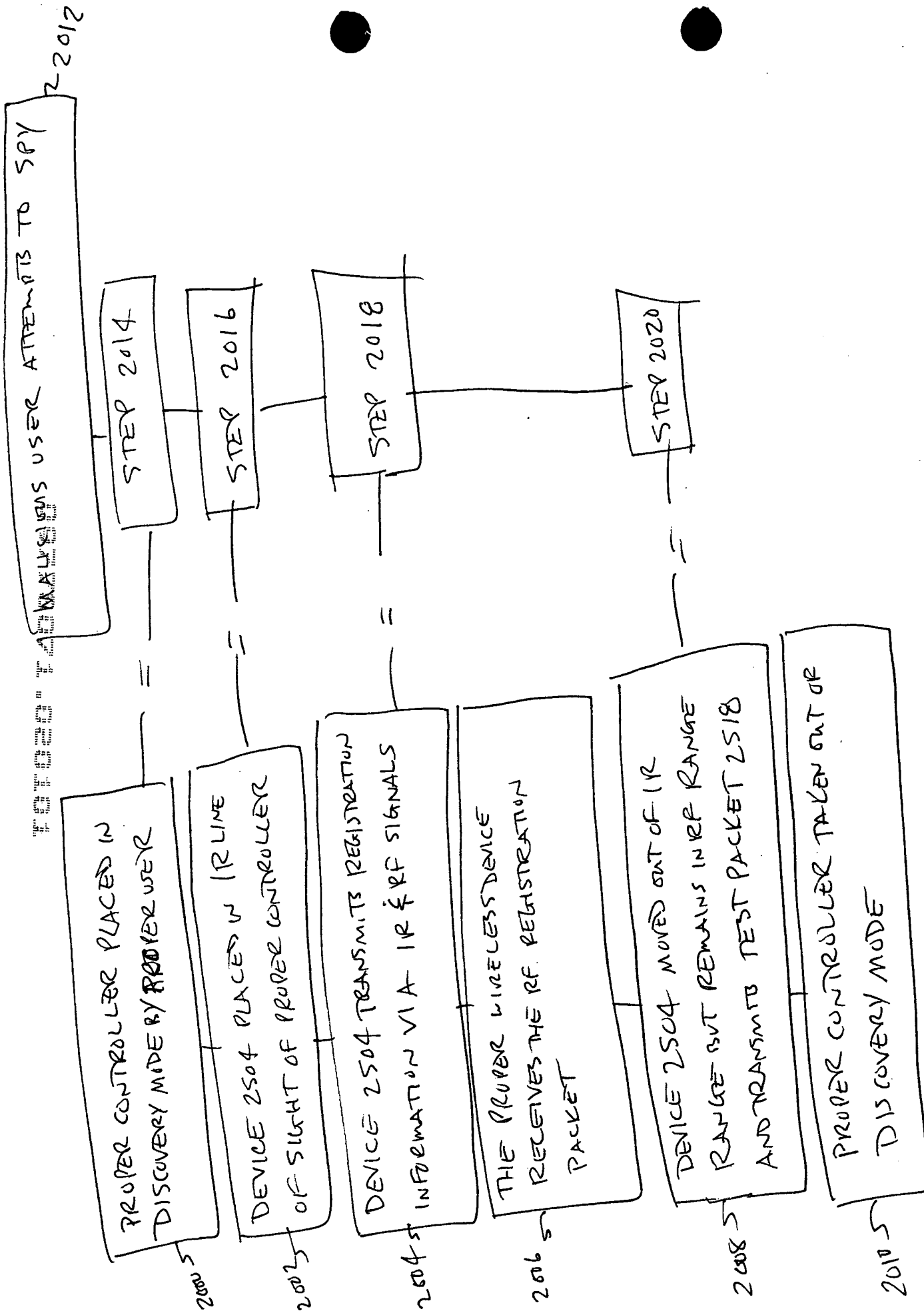


Fig. 7

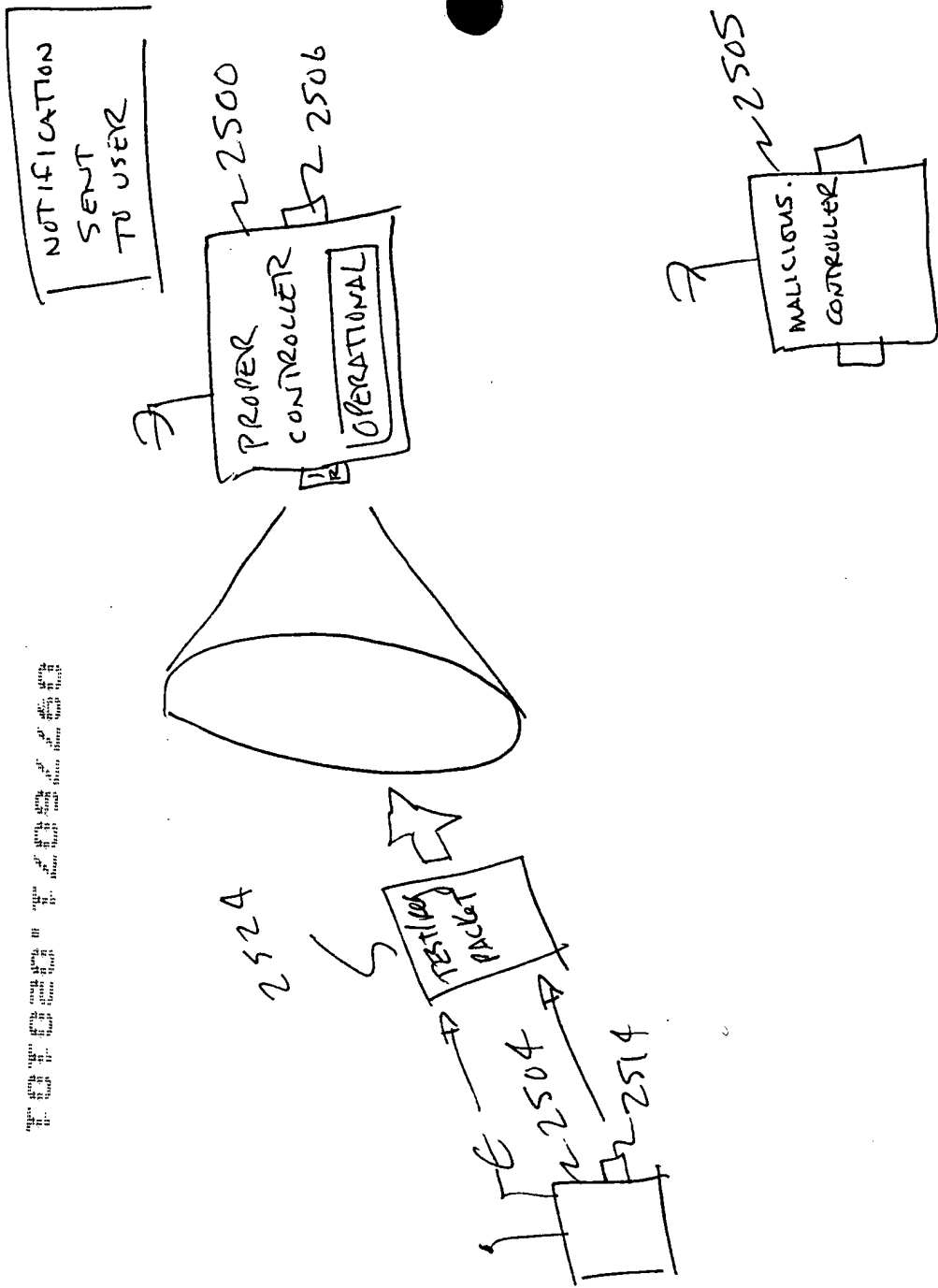


FIG. 8



FIG. 9

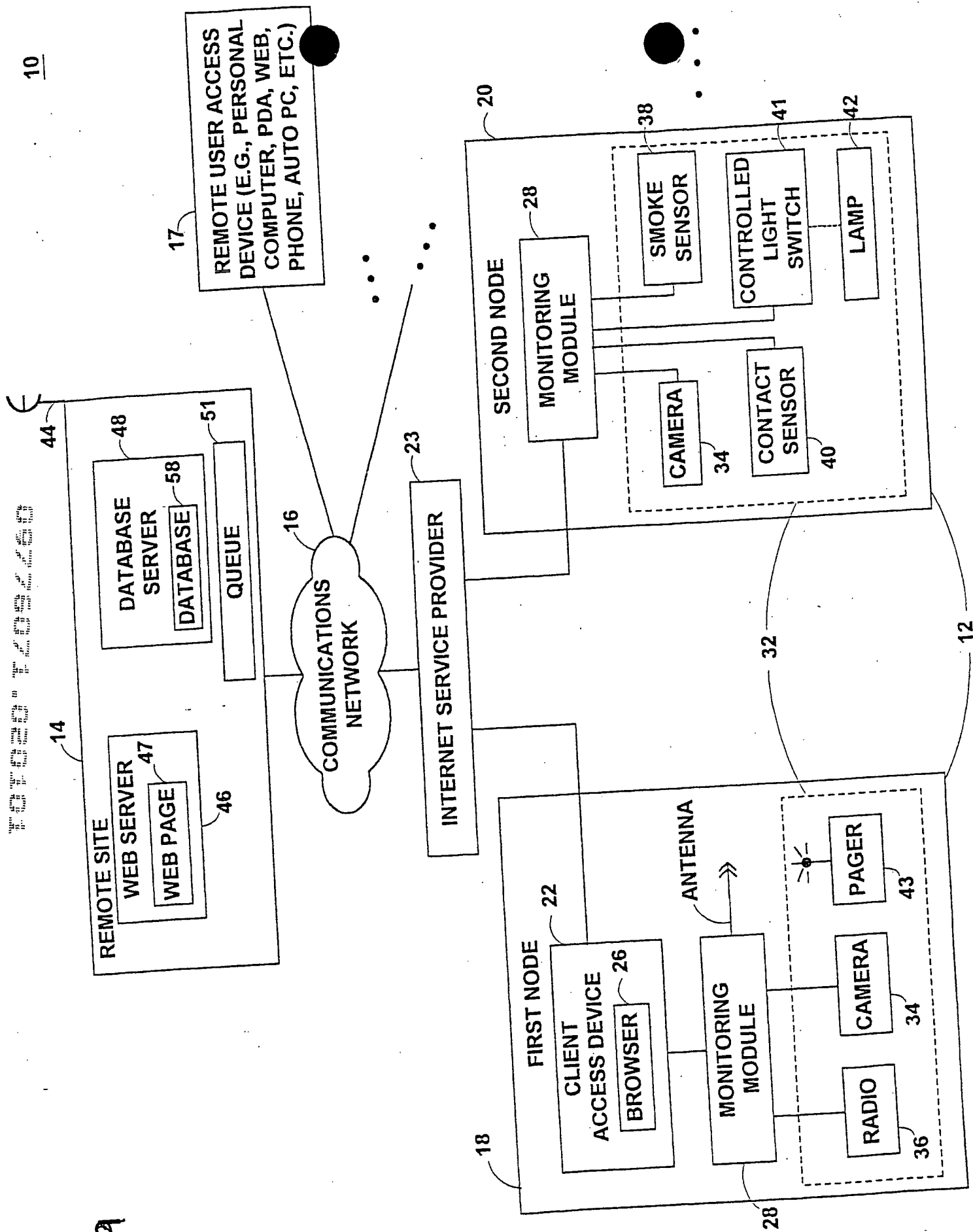
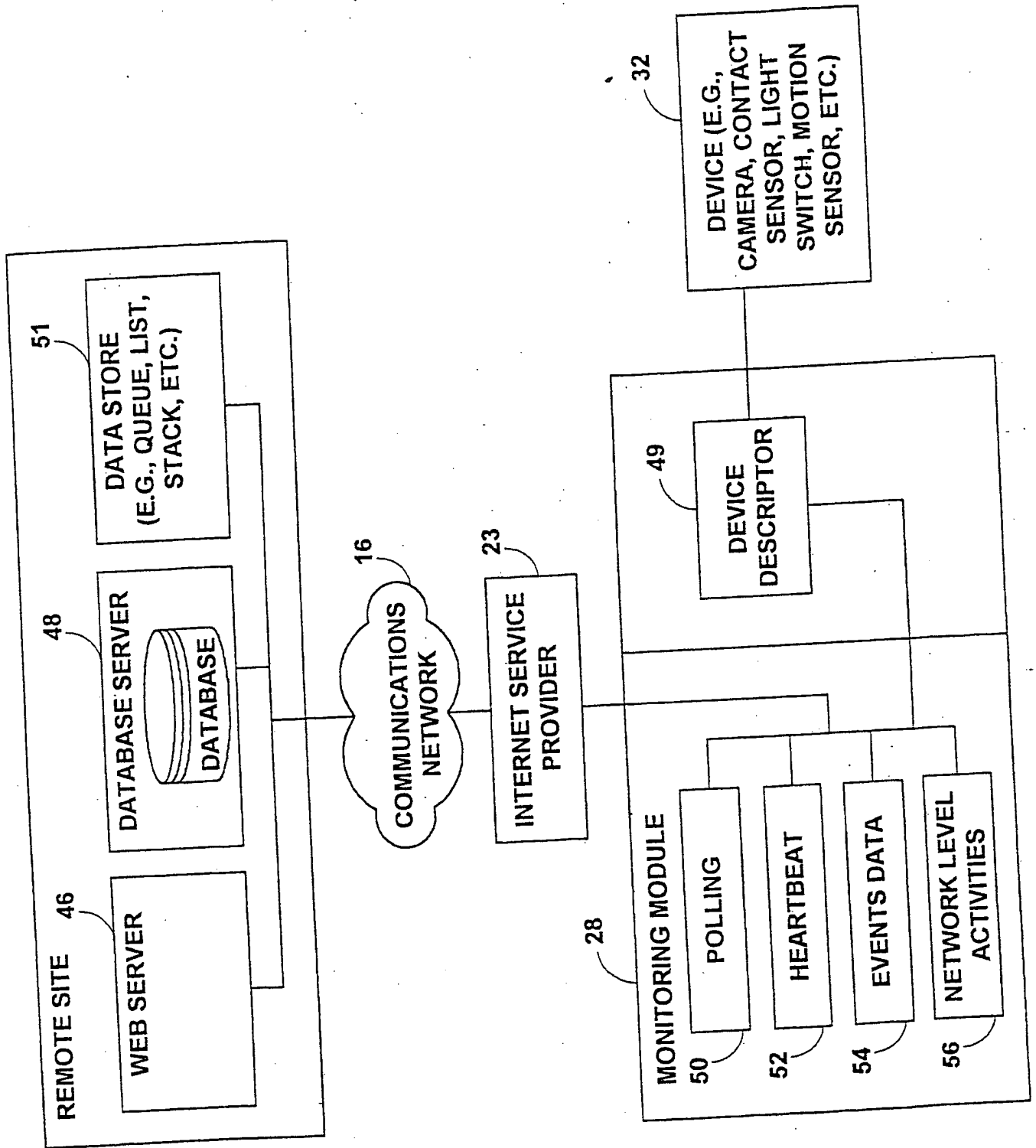


FIG. 10



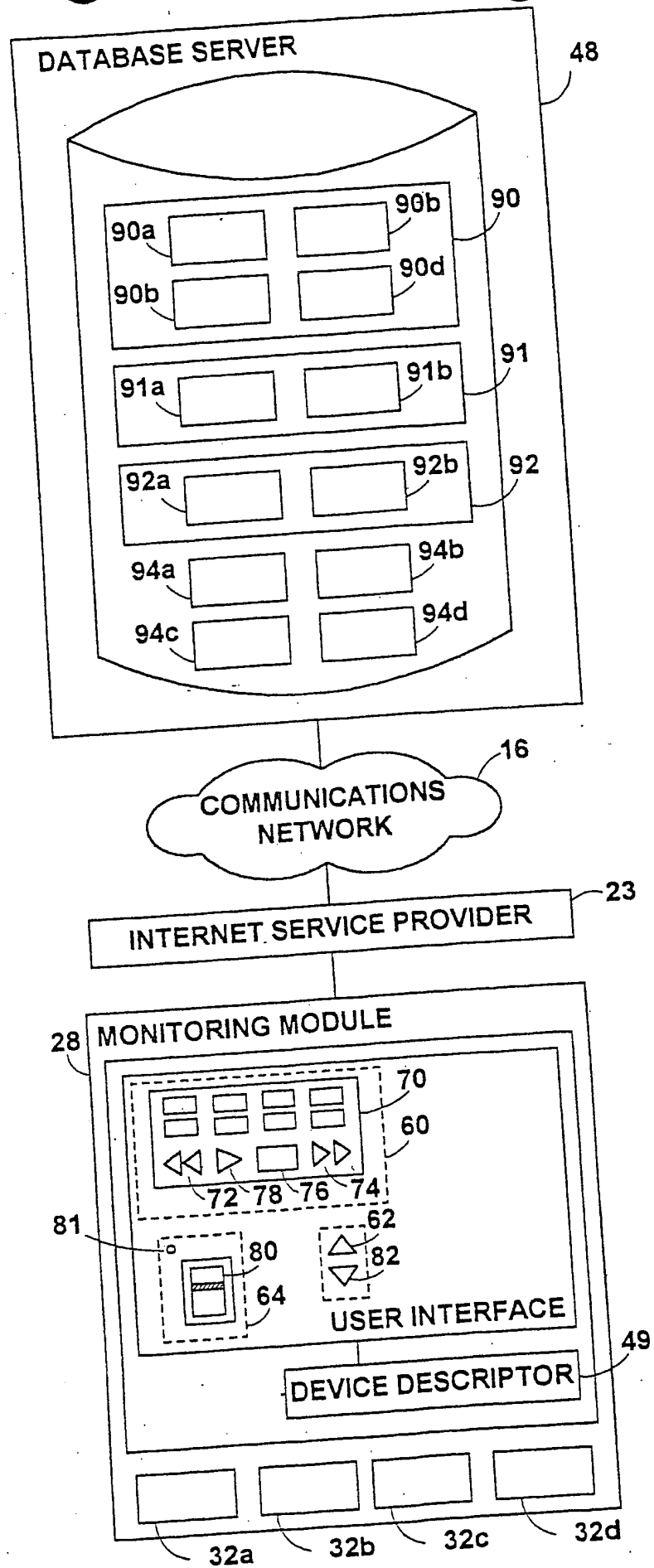


FIG. 11